

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application.

Listing of Claims:

1. (Currently Amended) A plasma display panel having a pair of substrates with at least one transparent front side and positioned to face each other so that discharge spaces are formed between the substrates comprising:

a front substrate; having

display electrodes provided on the front substrate, the display electrode including a transparent electrode and a bus electrode disposed on a side of the transparent electrode opposite the front substrate; ~~with scan electrodes and sustain electrodes, and~~

a light-shield ~~light-shields~~ formed on a non- discharge area between the display electrodes;

and

a rear substrate having phosphor layers to emit light by discharge, wherein

~~the display electrode comprises a transparent electrode and a bus electrode;~~

~~the bus electrode includes a plurality of electrode layers; and at least one of the electrode layers is composed of a black layer with a product of a resistivity and a layer thickness of not larger than $2 \Omega\text{cm}^2$ and the light-shield is composed of a black layer with a resistivity of not smaller than $1 \times 10^6 \Omega\text{cm}$, and~~

the light-shield extends from the front substrate along a side of the transparent layer to the black layer.

2. (Currently Amended) A plasma display panel having a pair of substrates with at least one transparent front side and positioned to face each other so that discharge spaces are formed between the substrates comprising:

a front substrate; having

display electrodes provided on the front substrate, the display electrode including a transparent electrode and a bus electrode; with scan electrodes and sustain electrodes, and

a light-shield formed on a non- discharge area between the display electrodes;

and

a rear substrate having phosphor layers to emit light by discharge, wherein

~~the display electrode comprises a transparent electrode and a bus electrode;~~

the bus electrode includes ~~a plurality of electrode layers;~~ at least one of the ~~electrode layers is composed of a black layer with a product of a resistivity and a layer thickness of not larger than $2 \Omega\text{cm}^2$ and the light-shield is composed of a black layer with a resistivity of not smaller than $1 \times 10^6 \Omega\text{cm}$; and~~

~~the display electrode and the light-shield are electrically insulated~~the black layer and the light-shield are composed of the same material and also the black layer and the light-shield are insulated electrically each other.

3. (Previously Presented) The plasma display panel of claim 1, wherein the black layer includes at least a black pigment and a conductive material.

4. (Original) The plasma display panel of claim 3, wherein the conductive material is an oxide including one of ruthenium and ruthenium oxide.

5. (Original) The plasma display panel of claim 3, wherein the conductive material is a metal conductive material.

6. (Original) The plasma display panel of claim 5, wherein the metal conductive material includes at least one of Ag, Cu, Pd, Pt and Au.

7. (Previously Presented) The plasma display panel of claim 2, wherein the black layer includes at least a black pigment and a conductive material.

8. (Previously Presented) The plasma display panel of claim 7, wherein the conductive material is an oxide including one of ruthenium and ruthenium oxide.

9. (Previously Presented) The plasma display panel of claim 7, wherein the conductive material is a metal conductive material.

10. (Previously Presented) The plasma display panel of claim 9, wherein the metal conductive material includes at least one of Ag, Cu, Pd, Pt and Au.